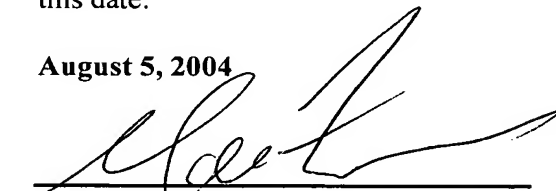


*JPW*

PATENT  
Docket No. INTEL/16404

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicant(s):	)	I hereby certify that this paper and the
	)	documents referred to as enclosed
<b>ZIMMER et al.</b>	)	therewith are being deposited with the
	)	United States Postal Service as first
U.S. Serial No.: 10/608,326	)	class mail, postage prepaid, in an
	)	envelope addressed to Commissioner
For: "Methods and Apparatus to	)	for Patents, P.O. Box 1450,
Provide Secure Firmware Storage	)	Alexandria, Virginia 22313-1450 on
and Service Access"	)	this date:
	)	
Filed: June 27, 2003	)	<b>August 5, 2004</b>
	)	
Assignee: Intel Corporation	)	
	)	
Group Art Unit: 2183	)	
	)	Mark C. Zimmerman
Examiner: Unknown	)	Attorney for Applicant(s)
	)	Registration No. 44,006

**STATUS LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

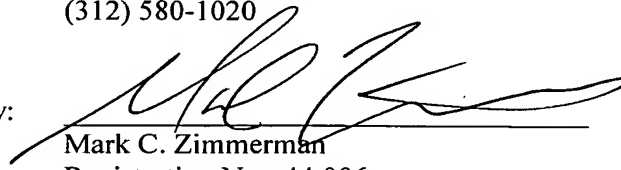
Kindly advise when an Office action can be expected in the above-referenced  
matter.

Respectfully submitted,

GROSSMAN & FLIGHT, LLC  
20 North Wacker Drive  
Suite 4220  
Chicago, Illinois 60606  
(312) 580-1020

August 5, 2004

By:

  
Mark C. Zimmerman  
Registration No.: 44,006

Attorneys for Intel Corporation